



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2841
Examiner: Ishwarbhai B Patel

In Re PATENT APPLICATION Of:
Applicants : Norio TAKAHASHI

Appln. No. : 10/775,232

Filed : February 11, 2004

For : MODULE CIRCUIT BOARD FOR
SEMICONDUCTOR DEVICE
HAVING BARRIERS TO ISOLATE
I/O TERMINALS FROM SOLDER

Attorney Ref.: F97ED0740-DIV3

PETITION FOR
ONE-MONTH
EXTENSION OF TIME

April 13, 2007

Commissioner for Patents
P.O. Box 1450
Alexandria, Va 22313-1450


Sir:

It is respectfully requested that the period for replying to the Examiner's Action dated December 22, 2006, be extended for one month under the provisions of 37 C.F.R. 1.136(a). Please charge the applicable extension fee of \$120.00 pursuant to 37 C.F.R. 1.17(a)(1) to our Account No. 50-0945.

Should any further fees be needed, please charge it to the Account and advise us accordingly. A duplicate copy of this paper is enclosed.

Respectfully submitted,

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